

SPECIFICATION FOR APPROVAL

(•) Preliminary Specification
 () Final Specification

Title

24.0" WUXGA TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LM240WU8
SUFFIX	SLE1

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE	APPROVED BY	SIGNATURE DATE
/		W.G. Kweon / G.Manager	
		REVIEWED BY	
		J.W. Hyun / Manager [C]	
/		Y.S. Chung / Manager [M]	
		J.C. Yim / Manager [P]	
/		PREPARED BY S.J. Yeom / Engineer	
Please return 1 copy for your o your signature and co		Product Engineering LG Display Co., I	



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RECORD OF REVISIONS

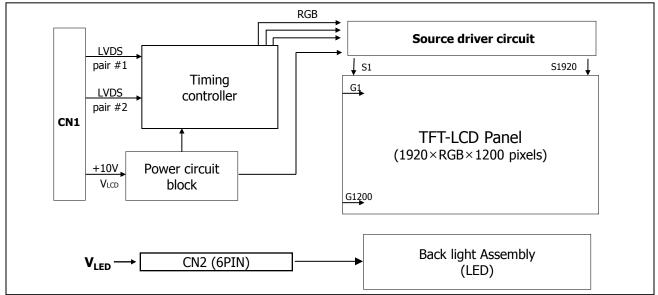
Revision No	Revision Date	Page	Description
0.0	Oct.28.2015	-	First Draft, Preliminary Specifications



1. General Description

LM240WU8-SLE1 is a Color Active Matrix Liquid Crystal Display with a Light Emitting Diode (White LED) backlight system without LED driver. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 24 inch diagonally measured active display area with WUXGA resolution (1200 vertical by 1920horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,78M colors with A-FRC (Advanced Frame Rate Control). It has been designed to apply the 8Bit 2 port LVDS interface.

It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



[Figure 1] Block diagram

General Features

Active Screen Size	24.1 inches(61.13cm) diagonal (Aspect ratio 16:10)
Outline Dimension	546.4(H) x 352.0(V) x 11.7(D) mm (Typ.)
Pixel Pitch	0.270 mm x 0.270 mm
Pixel Format	1920 horiz. By 1200 vert. Pixels RGB stripes arrangement
Color Depth	16,78M colors (6bit + A-FRC)
Luminance, White	300 cd/m ² (Center 1 Point, Typ.)
Viewing Angle(CR>10)	View Angle Free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total TBD Watt (Typ.) (TBD Watt @VLCD_mosaic, 13.4 Watt@Is=130mA)
Weight	TBD g (typ.)
Display Operating Mode	Transmissive mode, normally black
Module type	Non- borderless
Panel type	Forward
Surface Treatment	Anti-Glare treatment of the front polarizer (Haze25%, 3H)

Ver. 0.0

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Parameter	Symbol	Valu	les	Units	Notes	
Palailletei	Symbol	Min	Max	Onits	Notes	
Power Input Voltage	VLCD	-0.3	11.0	Vdc	at 25 \pm 2°C	
Operating Temperature	Тор	0	50	°C		
Storage Temperature	Тѕт	-20	60	°C		
Operating Ambient Humidity	Нор	10	90	%RH	1, 2, 3	
Storage Humidity	Нѕт	10	90	%RH		
LCM Surface Temperature (Operation)	T _{Surface}	0	65	Ĉ	1, 4	

Note : 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.

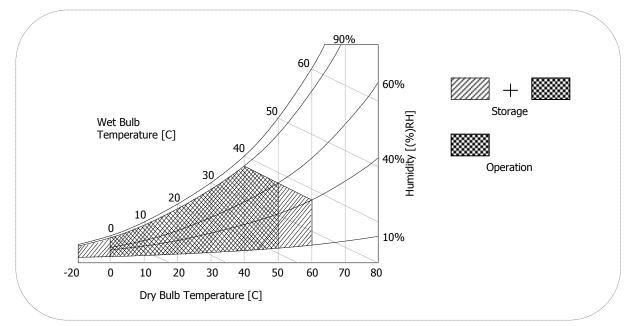
2. Maximum Storage Humidity is up to 40° C, 70% RH only for 4 corner light leakage Mura.

3. Storage condition is guaranteed under packing condition

4. LCM Surface Temperature should be Min. 0°C and Max. 65°C under the VLCD=10.0V,

fV=60Hz, 25°C ambient Temp. no humidity control and LED string current is typical value.

FIG.2 Temperature and relative humidity



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

Table 2-1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Values	Unit	Notes		
Parameter	Symbol	Min	Тур	Max	Unit	Notes	
MODULE :							
Power Supply Input Voltage	VLCD	9.5	10	10.5	Vdc		
Permissive Power Input Ripple	VdRF			0.4	V	1	
Devuer Currely Input Current	ILCD_Mosaic	-	TBD	TBD	mA	2	
Power Supply Input Current	ILCD_White	-	TBD	TBD	mA	3	
Dower Concurrentian	Pc_Mosaic	-	TBD	TBD	Watt	2	
Power Consumption	PcLCD_White	-	TBD	TBD	Watt	3	
Rush current	Irush	-	-	3.0	A	4	

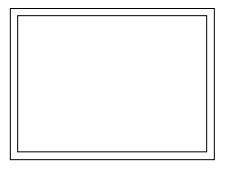
Note :

1. Permissive power ripple should be measured under $V_{LCD} = 10.0V$, 25°C, fV(frame frequency)=MAX condition and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20Mhz. See the next page.

- 2. The specified current and power consumption are under the V_{LCD} =10.0V, 25± 2°C,fV=60Hz condition whereas Typical Power Pattern [Mosaic] shown in the [Figure 2] is displayed.
- 3. The current is specified at the maximum current pattern.
- 4. Maximum Condition of Inrush current : The duration of rush current is about 5ms and rising time of power Input is 500us \pm 20%.(min.).
- 5. VLCD level must be measured from LCM PCB's two points, between VIN and LCM Ground. The measured level need to meet the Power supply input voltage spec. (Test condition : maximum power pattern , $25\pm 2^{\circ}$ C, fV=60Hz)

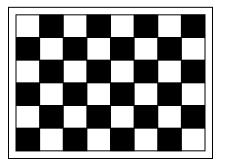


• Permissive Power input ripple (V_{LCD} =10V, 25°C, fv (frame frequency)=MAX condition)

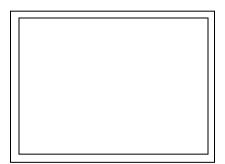


Full White Pattern

• Power consumption (V_{LCD} =10V, 25°C, fV (frame frequency=60Hz condition)



Typical power Pattern



Maximum power Pattern

FIG.3 Mosaic pattern(8x6) & Full White Pattern for power consumption measurement

Table 2-2. LED Bar ELECTRICAL CHARACTERISTICS

Devenueter	Cumhal		Values		Nataa	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
LED String Current	Is	-	(130)	(135)	mA	1, 2, 5
LED String Voltage	Vs	(48.1)	(51.5)	(54.9)	V	1, 5
Power Consumption	PBar	-	(13.4)	(14.3)	Watt	1, 2, 4
LED Life Time	LED_LT	30,000	-	-	Hrs	3

Notes) The LED Bar consists of 34 LED packages, 2 strings (parallel) x 17 packages (serial)

LED driver design guide

: The design of the LED driver must have specifications for the LED in LCD Assembly.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.

So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs. When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

- 1. Specified values are for a single LED bar.
- 2. The specified current is defined as the input current for a single LED string with 100% duty cycle.
- 3. The LED life time is defined as the time when brightness of LED packages become 50% or less than the initial value under the conditions at Ta = $25\pm2^{\circ}$ C and LED string current is typical value.
- 4. The power consumption shown above does not include loss of external driver. The twised power consumption is calculated as $P_{int} = \sqrt{c(T_{int})} \times Ic(T_{int}) \times Vc(T_{int})$

The typical power consumption is calculated as $P_{Bar} = Vs(Typ.) \times Is(Typ.) \times No.$ of strings. The maximum power consumption is calculated as $P_{Bar} = Vs(Max.) \times Is(Typ.) \times No.$ of strings.

5. LED operating conditions are must not exceed Max. ratings.

3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(CN1) : GT103-30S-H23-D (LSM), KDF71G-30S-1H(Hirose) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

	Table 3.	MODULE	CONNECTOR(CN1) PIN	CONFIGURATION
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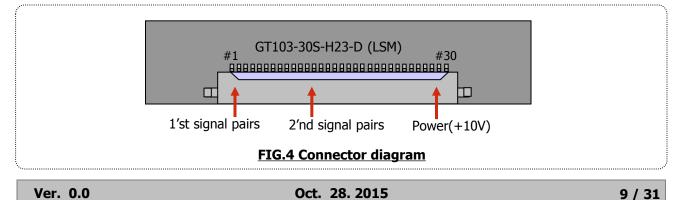
No	Symbol	Description	No	Symbol	Symbol
1	FR0M	Minus signal of odd channel 0 (LVDS)	16	SR1P	Plus signal of even channel 1 (LVDS)
2	FR0P	Plus signal of odd channel 0 (LVDS)	17	GND	Ground
3	FR1M	Minus signal of odd channel 1 (LVDS)	18	SR2M	Minus signal of even channel 2 (LVDS)
4	FR1P	Plus signal of odd channel 1 (LVDS)	19	SR2P	Plus signal of even channel 2 (LVDS)
5	FR2M	Minus signal of odd channel 2 (LVDS)	20	SCLKINM	Minus signal of even clock channel (LVDS)
6	FR2P	Plus signal of odd channel 2 (LVDS)	21	SCLKINP	Plus signal of even clock channel (LVDS)
7	GND	Ground	22	SR3M	Minus signal of even channel 3 (LVDS)
8	FCLKINM	Minus signal of odd clock channel (LVDS)	23	SR3P	Plus signal of even channel 3 (LVDS)
9	FCLKINP	Plus signal of odd clock channel (LVDS)	24	GND	Ground
10	FR3M	Minus signal of odd channel 3 (LVDS)	25	NC	No Connection (I2C Serial interface for LCM)
11	FR3P	Plus signal of odd channel 3 (LVDS)	26	NC	No Connection.(I2C Serial interface for LCM)
12	SR0M	Minus signal of even channel 0 (LVDS)	27	ITLC	Interlace Mode Selection 'H'(3.3V) = Enable , 'L' = Disable
13	SR0P	Plus signal of even channel 0 (LVDS)	28	VLCD	Power Supply +10.0V
14	GND	Ground	29	VLCD	Power Supply +10.0V
15	SR1M	Minus signal of even channel 1 (LVDS)	30	VLCD	Power Supply +10.0V

Notes : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

2. All VLCD (power input) pins should be connected together.

3. All Input levels of LVDS signals are based on the EIA 644 Standard.

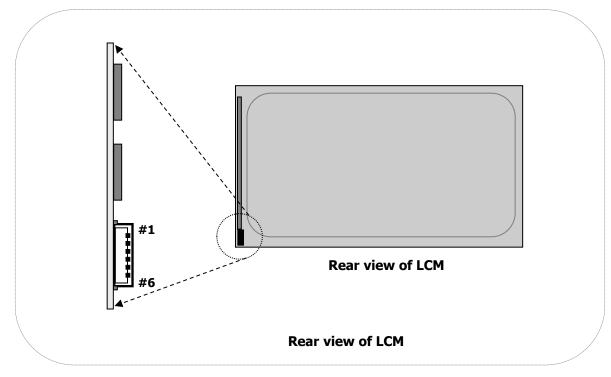
4. PWM_OUT is a reference signal for LED PWM control. This PWM signal is synchronized with vertical frequency. Its frequency is 5 times of vertical frequency, and its duty ratio is 50%. If the system don't use this pin, do not connect.



3-2-2. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2)

The LED interface connector is a model SM06B-SHJH(HF)_Manufactured by JST or equivalent. The mating connector is a SHJP-06V-S(HF), SHJP-06V-A-K(HF) or equivalent. The pin configuration for the connector is shown in the table below.

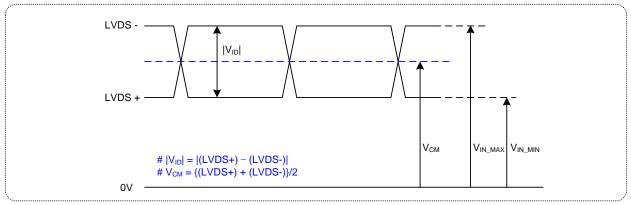
Pin	Symbol	Description	Notes
1	FB1	Channel1 Current Feedback	
2	NC	No connection	
3	VLED	LED Power Supply	
4	VLED	LED Power Supply	
5	NC	No connection	
6	FB2	Channel2 Current Feedback	





3-3. LVDS characteristics

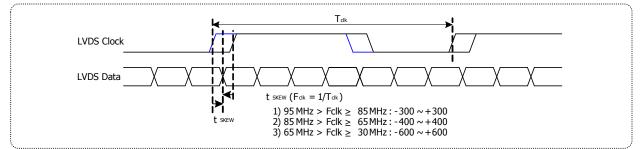
3-3-1. DC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	$ V_{ID} $	150	600	mV	-
LVDS Common mode Voltage	V _{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V _{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔVCM	-	250	mV	-

Notes : Dose not have any Noise & Peaking in LVDS Signal

3-3-2. AC Specification

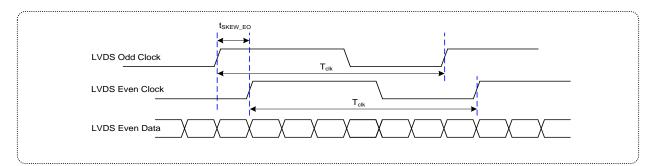


Description	Symbol	Min	Мах	Unit	Notes
	t _{skew}	- 300	+ 300	ps	95MHz > Fclk ≥ 85MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 30MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-

Note 1 :

LGD recommend the SI should be adjust the SSC deviation and modulation frequency in order not to happen any kinds of defect phenomenon.

3-3-2. AC Specification



< Clock skew margin between channel >

3-3-3. LVDS data format(8bit,VESA)

1) LVDS 2 Port

			<			Tclk											
RCLK +			←		<u>(* 4/7</u> Tclk * 1/	7	↓ 1	Fclk * 3/7	″→						MSB	R7]
RXinO0 +/-	OR3	OR2	OR1	OR0	060	OR5	OR4	OR3	OR2	OR1	OR0	OG0	OR5	OR4		R6 R5	
RXinO1 +/-	OG4	OG3	OG2	OG1	OB1	ОВО	OG5	OG4	OG3	OG2	OG1	OB1	ОВО	OG5		R5 R4	
RXinO2 +/-	OB5	OB4	ОВЗ	OB2	DE	VSYNC	HSYNC	OB5	ОВ4	ОВЗ	OB2	DE	VSYNC	HSYNC		R3 R2	
RXinO3 +/-	OG7	OG6	OR7	OR6	X	ОВ7	OB6	OG7	OG6	OR7	OR6	X	OB7	OB6		R1	
RXinE0 +/-	ER3	ER2	ER1	ERO	EG0	ER5	ER4	ER3	ER2	ER1	ERO	EG0	ER5	ER4	LSB	R0]
RXinE1 +/-	EG4	EG3	EG2	EG1	EB1	EBO	EG5	EG4	EG3	EG2	EG1	EB1	EBO	EG5		DD = 1st EN = 2nd	
RXinE2 +/-	EB5	EB4	EB3	EB2	DE	VSYNC	HSYNC	EB5	EB4	ЕВЗ	EB2	DE	VSYNC	HSYNC			
RXinE3 +/-	EG7	EG6	ER7	ER6	X	ЕВ7	EB6	EG7	EG6	ER7	ER6	X	EB7	EB6			
	Pre	evious(N	I-1)th Cy	vcle>			-Curre	ent(Nth)	Cycle		>	-Next	N+1)th	Cycle—			

< LVDS Data Format >

3-4. Signal Timing Specifications

This is signal timing required at the input of the TMDS transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Period	tclk	12.19	12.98	16.06	ns	Pixel frequency
DCLK	Frequency	fclk	62.24	77	82	MHz	: Typ. 154MHz
	Period	thp	1013	1040	1048	tCLK	
Hsync	Horizontal Valid	tHV	960	960	960	tCLK	
risync	Horizontal Blank	tНв	53	80	88		
	Frequency	fн	61.13	74.1	81	KHz	
	Period	tvp	1229	1235	1390	tHP	
	Vertical Valid	tvv	1200	1200	1200	tHP	
Vsync	Vertical Blank	tvв	29	35	190	tHP	
	Frequency	fv	49.5	60	61	Hz	

Table 4. TIMING TABLE

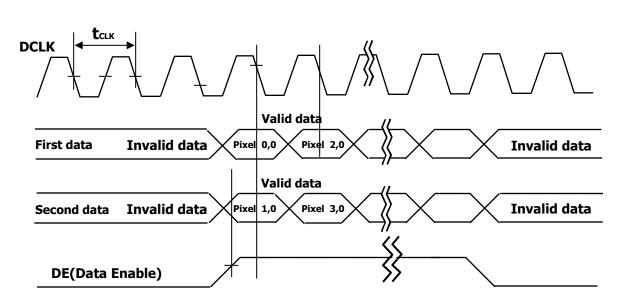
Note: Hsync period and Hsync width-active should be even number times of tCLK. If the value is odd number times of tCLK, display control signal can be asynchronous. In order to operate this LCM a Hsync, Vsync, and DE(data enable) signals should be used.

- 1. The Input of Hsync & Vsync signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum for EMI, add some additional clock to minimum value for clock margin.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. Vsync and Hsync should be keep the above specification.
- 4. Hsync Period, Hsync Width, and Horizontal Back Porch should be any times of character number(4).
- 5. The polarity of Hsync, Vsync is not restricted.

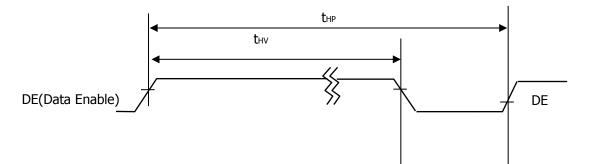


3-5. Signal Timing Waveforms

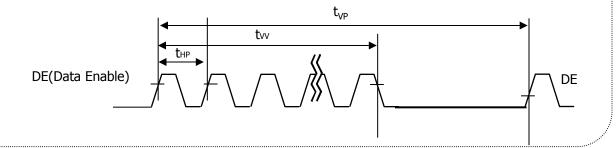
1. DCLK, DE, DATA waveforms



2. Horizontal waveform



3. Vertical waveform



3-6. Color Input Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

												Ι	npu	t Co	olor	Da	ta									
						RE	D							GRE	EN							BL	UE			
	Color		MS	SB					LS	SB	MS	SB					L	SB	MS	SB					L	SB
			R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	во
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED															•											
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN							•																			
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE							•								•											
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Ver. (0.0							(Oct.	2	8.	201	5											15	5/3	31

3-7. Power Sequence

3-7-1. Power Sequence

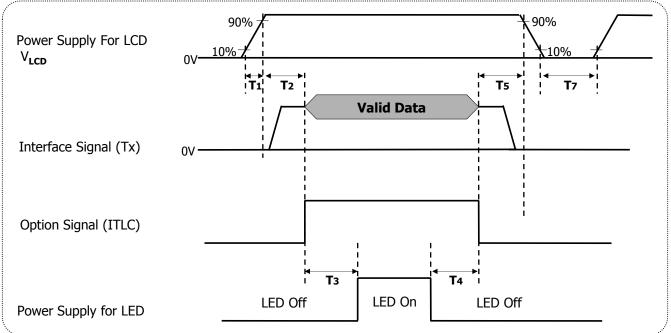


Table 6.POWER SEQUENCE

Parameter		Values								
Parameter	Min	Тур	Max	Units						
T1	0.5	-	10	ms						
T2	0.01	-	50	ms						
Т3	500	-	-	ms						
T4	200	-	-	ms						
Т5	0.01	-	50	ms						
Т7	1000		-	ms						

Notes: 1. Please VLCD power on only after connecting interface cable to LCD.

- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
- 4. The invalid signal means out of the signal timing specification which define as page 17.
- 5. The above power sequence should be satisfied the basic power on/off and resolution, timing transition.
- 6. LED power must be turn on after power supply for LCD and interface signal are valid.
- 7. Recommend to follow Power sequence at these case
 - -.AC/DC Power On/Off

-.Mode change (Resolution, frequency, timing, sleep mode, Color depth change, etc.) If not to follow power sequence, there is a risk of abnormal display.



3-7-2. V_{LCD} Power Dip Condition

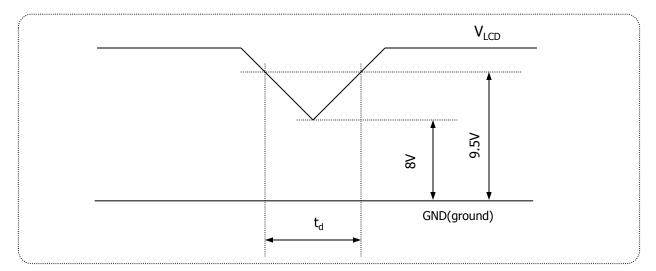


FIG.5 Power dip condition

1) Dip condition

 $8V \leq V_{LCD} < ~9.5V$, $~t_d {\leq} 20ms$

2) V_{LCD} < 8V

 V_{LCD} -dip conditions should also follow the Power On/Off conditions for supply voltage.



4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 ° and aperture 1 degree. FIG. 4 presents additional information concerning the measurement equipment and method.

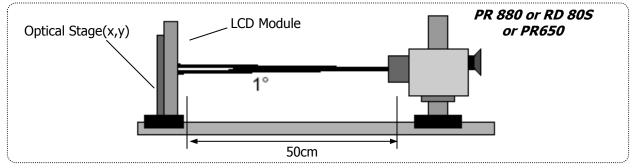


FIG.4 Optical Characteristic Measurement Equipment and Method

Table 7. OPTICAL CHARACTERISTICS

(Ta=25 °C, V_{LCD}=10V, f_V=60Hz Dclk=154MHz, I_{BL}=130mA)

Davana	•	Cumhal		Values		Unite	Notos
Parame	eter	Symbol	Min	Тур	Мах	Units	Notes
Contrast Ratio		CR	700	1000	-		1
Surface Luminance, v	white	L _{WH}	250	300	-	cd/m ²	2
Luminance Variation		δ_{WHITE}	75	-	-	%	3
Response Time	Gray To Gray	T_{GTG}_{AVR}	-	14	28	ms	4
	RED	Rx		TBD			
		Ry	1	TBD			
	GREEN	Gx	1	TBD			
Color Coordinates [CIE1931]		Gy] Тур	TBD	Тур		
(By PR650)	BLUE	Bx	-0.03	TBD	+0.03		
		Ву		TBD			
	WHITE	Wx]	0.313			
		Wy		0.329			
Color Shift	Horizontal	$\theta_{\text{CST}_\text{H}}$	-	140	-	Degree	5
(Avg. Δu′v′ < 0.02)	Vertical	$\theta_{\text{CST}_\text{V}}$	-	100	-	Degree	Э
Viewing Angle (CR>1	.0)						
Conorol	Horizontal	θ_{H}	170	178	-	Degree	6
General	Vertical	θ_V	170	178	-	Degree	O
GSR @ 60dgree	Horizontal	$\delta_{\text{Gamma}_{\text{H}}}$	-	-	20	<i></i>	_
Gamma shift rate) Vertical		δ_{Gamma_V}	-	-	20	%	7
Gray Scale		-		2.2			8

Ver. 0.0



Notes 1. Contrast Ratio(CR) is defined mathematically as : (By PR880)

 $Contrast Ratio = \frac{Surface Luminance with all white pixels}{Surface Luminance with all black pixels}$

It is measured at center point(Location P1)

- 2. Surface luminance(LWH) is luminance value at Center 1 point(P1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.8 (By PR880)
- 3. The variation in surface luminance , δ WHITE is defined as : (By PR880)

 $\delta_{WHITE} = \frac{\text{Minimum}(L_{P1}, L_{P2}, \dots, L_{P9})}{\text{Maximum}(L_{P1}, L_{P2}, \dots, L_{P9})} \times 100$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations. For more information see FIG.8

- 4. Gray to gray response time is the time required for the display to transition from gray to gray. For additional information see Table 9. (By RD80S)
- 5. Color shift is the angle at which the average color difference for all Macbeth is lower than 0.02. For more information see FIG.9 (By EZ Contrast)

- Color difference ($\Delta u'v'$)

$$u' = \frac{4x}{-2x+12y+3} \qquad v' = \frac{9y}{-2x+12y+3} \qquad \Delta u'v' = \sqrt{(u'_1 - u'_2)^2 + (v'_1 - v'_2)^2}$$

$$Avg(\Delta u'v') = \frac{\sum_{i=1}^{24} (\Delta u'v')i}{24} \qquad u'1, v'1 : u'v' \text{ value at viewing angle direction} \\ u'2, v'2 : u'v' \text{ value at front } (\theta=0) \\ i : \text{ Macbeth chart number (Define 23 page)}$$

- Pattern size : 25% Box size
- Viewing angle direction of color shift : Horizontal, Vertical

24

- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.10 (By PR880)
- 7. GSR is the rate of gamma shift at up, down, left and right 60 degree viewing angle compare with center gamma. For more information see FIG.11 and FIG.12 (By EZ Contrast) - GSR (δ_{Gamma}) is defined as :

$$GSR = \left(1 - \frac{\text{View angle Gamma Value (Up, Down, Reft, Light 60 Degree)}}{\text{Center Gamma Value (0 Degree)}}\right) \times 100$$

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Notes 8. Gamma Value is approximately 2.2. For more information see Table 10.

Measuring point for surface luminance & measuring point for luminance variation.

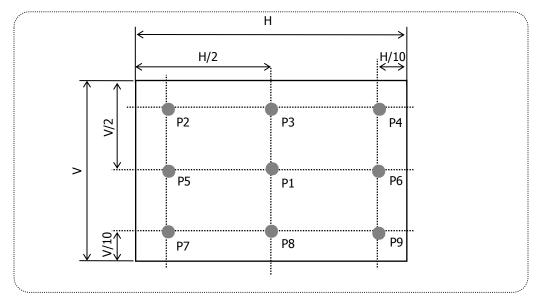


FIG.8 Measure Point for Luminance

The Gray to Gray response time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray ".

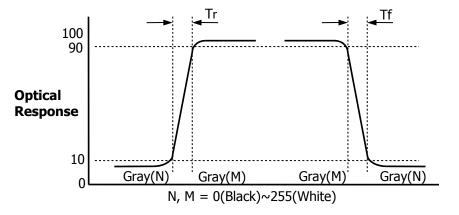
- Gray step : 5 Step
- TGTG_AVR is the total average time at rising time and falling time for "Gray To Gray ".
- if system use ODC (Over Driving Circuit) function, Gray to Gary response time may be 5ms~8ms GtG * it depends on Overshoot rate.

			R	ising Tim	е	
Gray to G	ray	G255	G191	G127	G63	G0
Falling Time	G255					
	G191			/		
	G127				/	
	G63					/
	G0					

Table 8. GTG Gray Table



G to G(BW) Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".



Color shift is defined as the following test pattern and color.

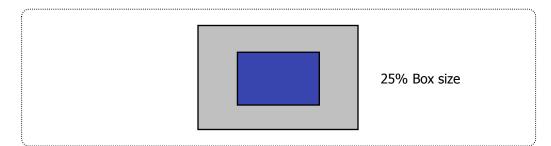


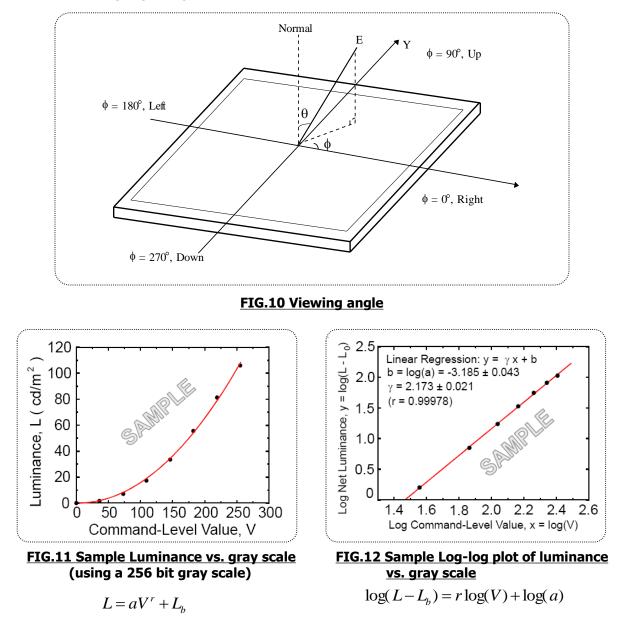
FIG.9 Color Shift Test Pattern

Average RGB values in Bruce RGB for Macbeth Chart

	Dark skin (i=1)	Light skin	Blue sky	Foliage	Blue flower	Bluish green
R	98	206	85	77	129	114
G	56	142	112	102	118	199
В	45	123	161	46	185	178
	Orange	Purplish blue	Moderate red	Purple	Yellow green	Orange yellow
R	219	56	211	76	160	230
G	104	69	67	39	193	162
В	24	174	87	86	58	29
	Blue	Green	Red	Yellow	Magenta	Cyan
R	26	72	197	241	207	35
G	32	148	27	212	62	126
В	145	65	37	36	151	172
	White	Neutral 8	Neutral 6.5	Neutral 5	Neutral 3.5	Black
R	240	206	155	110	63	22
G	240	206	155	110	63	22
В	240	206	155	110	63	22



Dimension of viewing angle range.



Here the Parameter a and γ relate the signal level V to the luminance L. The GAMMA we calculate from the log-log representation (FIG.11)



Table 9. Gray Scale Specification

Gray Level	Relative Luminance [%] (Typ.)
0	0.10
15	0.30
31	1.08
47	2.50
63	4.72
79	7.70
95	11.49
111	16.20
127	21.66
143	28.20
159	35.45
175	43.80
191	53.00
207	63.30
223	74.48
239	86.80
255	100

5. Mechanical Characteristics

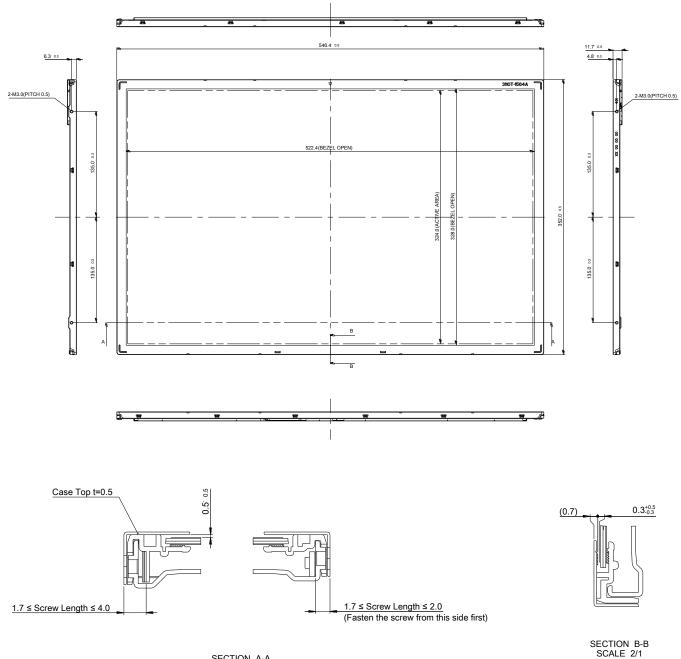
The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	546.4mm			
Outline Dimension	Vertical	352.0mm			
	Depth	11.7 mm			
Bezel Area	Horizontal	522.4mm			
Dezer Area	Vertical	328.0mm			
Active Dicplay Area	Horizontal	518.4mm			
Active Display Area	Vertical	324.0mm			
Weight	Typ : TBD g , Max : TBD g				
Surface Treatment	Anti-Glare treatment of the front polarize	er (Haze25%, 3H)			

Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.

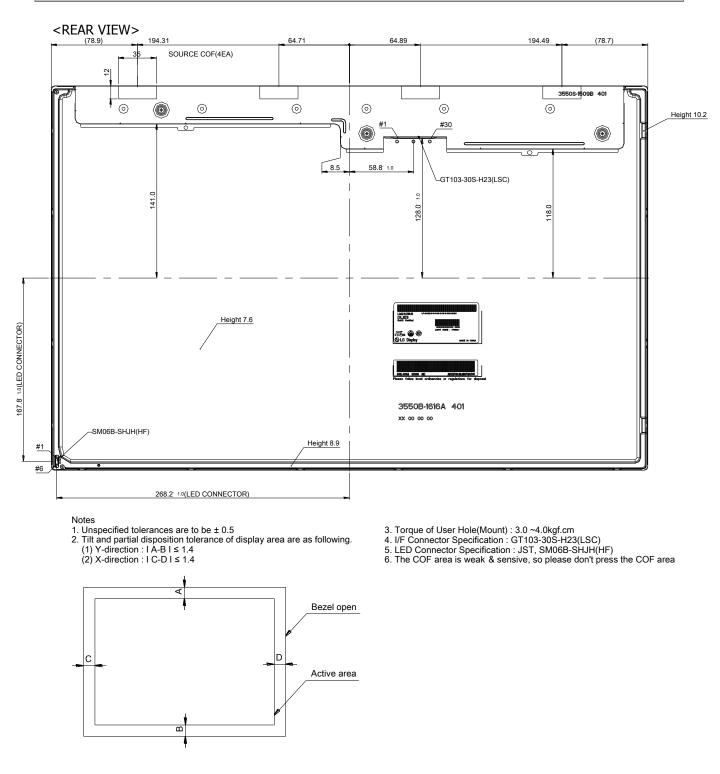


<FRONT VIEW>



SECTION A-A SCALE 2/1





LGD Highly recommendation :

System chassis or frame should be designed to keep the IPS Panel flat as it is vulnerable to panel light-leakage caused by deformation.



6. Reliability

Environment test condition

No	Test Item	Condition					
1	High temperature storage test	Ta= 60°C 240h					
2	Low temperature storage test	Ta= -20°C 240h					
3	High temperature operation test	Ta= 50°C 50%RH 240h					
4	Low temperature operation test	Ta= 0°C 240h					
5	Humidity condition Operation	Ta= 40 °C ,90%RH					
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min One time each direction					
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction					
6	Altitude operating storage / shipment	0 – 10,000 feet(3,048m) 0 - 40,000 feet(12,192m)					
7	Maximum Storage Humidity for 4 corner light leakage Mura.	Max 70%RH , Ta=40℃					

Note 1. Result Evaluation Criteria:

TFT-LCD panels test should take place after cooling enough at room temperature. In the standard condition, there should be no particular problems that may affect the display function.

7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc. Information Technology Equipment - Safety - Part 1 : General Requirements.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
 Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment - Safety - Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC). Information Technology Equipment - Safety - Part 1 : General Requirements

7-2. Environment

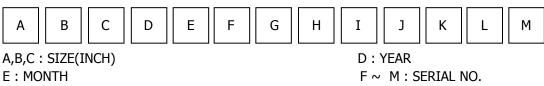
a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : TBD pcs
- b) Box Size : TBD mm X TBD mm X TBD mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in rear side.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) As The IPS panel is sensitive & slim, please recommend the metal frame of the system supports the panel by the double side-mount.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In Higher temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
 - (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) When LCMs are used for public display defects such as Yogore, image sticking can not be guarantee.
- (11) When this forward model is used as a reverse-type model (PCB on bottom side), LGD can not guarantee any defects of LCM.
- (12) If the ITLC pin is unused, LCM can not support "Interlaced Scan Method"
- (13) Please conduct image sticking test after 2-hour aging with Rolling Pattern and normal temperature.(25~40 ℃)

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.